said ceramic element [per layer]; said laminated ceramic electronic part [being characterized in that it satisfies] 10 satisfying the following requirements:

- [a] the thickness of each said ceramic layer is 10  $\mu$ m or less;
- [a] the number of [lamination of] said internal electrodes is 200 or more;
- (c) [a] the ratio of the thickness of each said internal electrode to the thickness of each said ceramic layer [(thickness of internal electrode/thickness of ceramic layer)] is 0.10 to 0.40; and
- (d) [a] the ratio [(volume of internal electrodes/volume of ceramic element)] of [a] the combined volume of said internal [electrode] electrodes to [a] the total volume of said ceramic element [(total volume of internal electrodes and ceramic)] is 0.10 to 0.30.

## REMARKS

The specification has been amended to improve the idiomatic usage of English. It is requested that the change be carefully reviewed by the Examiner and entered without objection.

Claim 1 has been amended to place the same in better form for U.S. practice.

Consideration of the application and allowance of claim 1 are earnestly solicited.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on May 27, 1998:

Respectfully submitted,

Steven I. Weisburd

Name of applicant, assignee or Registered Representative

> Signature May 27, 1998

Date of Signature

SIW:mzl/gb

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